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PATENT  
Attorney Docket No. ASC-044

#8/a  
12-26-02  
L. Spruell

Lee ok

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S): Eugene A. Fitzgerald et al.  
SERIAL NO.: 09/884,172 GROUP NO.: 2812  
FILING DATE: June 19, 2001 EXAMINER: Christopher W. Lattin  
TITLE: METHOD OF FABRICATING CMOS INVERTER AND  
INTEGRATED CIRCUITS UTILIZING STRAINED SILICON  
SURFACE CHANNEL MOSFETS

Commissioner for Patents  
Washington, D.C. 20231

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**AMENDMENT AND RESPONSE**

Sir:

This is an amendment to the above-identified application in response to the Office  
Action mailed on September 24, 2002.

**In the Claims**

Please cancel claims 2, 3, 14, and 15, and amend the claims in accordance with  
the clean and marked-up versions that accompany this paper.

**REMARKS**

In the Office Action mailed on September 24, 2002, the Examiner rejected claims  
1-3, 5-15, and 17-25 as obvious over Rim et al.; and claims 1, 4, 13, and 16 as  
unpatentable over Rim et al. in view of Chu et al. The Examiner's allowance of claims  
26 and 27 is noted with appreciation.